

Notification for expected changes to HTPA84x60d

This document is supposed to give a quick overview about changes that will be implemented to all HTPA84x60d and HTPA120x84d sensors with the next ASIC revision:

1. The EEPROM will be replaced by a Flash with larger memory, sample code will be provided.
2. The Flash is not accessible to be written by the customer.
3. The amplifier is modified so that the BPA settings is not required anymore. This register should not be written anymore.
4. The BIAS setting is now being set independently for the top and bottom half of the picture.
5. The improved amplifier allows a more homogenous picture after subtracting the electrical offsets.
6. The readout is modified in such a way that the PTAT, VDD and ATC will always be read. Please check the datasheet for a comparison on the future readout.
7. The calibration routine is open to changes to make use of the ATC to further improve a heat-shock compensation.
8. The internal clock (FCLK) is changed which can result in a different FPS.